



Savitribai Phule Shikshan Prasarak Mandal's
SKN SINHGAD COLLEGE OF ENGINEERING

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Department of Electronics & Telecommunication

Webinar on

“RTL Design and Functional Verification”

by Maven Silicon VLSI Training Center, Karnataka

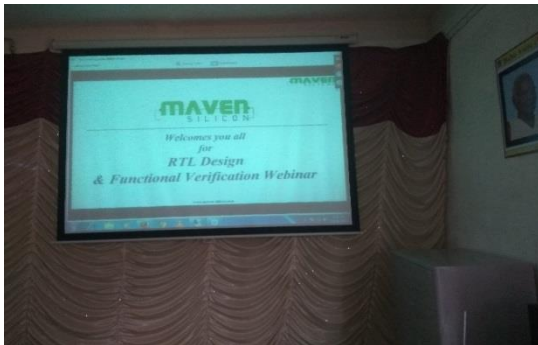
Date: 13th Jul , 2018

Conducted by: **Maven Silicon VLSI Training Center, Karnataka**

Coordinated By: **Prof. A. O. Mulani, Prof. Pise A.C**

Participated by: TE, BE E&TC(84), ME Electronics (03), Staff(10)

With full enthusiasm a webinar is organized under the banner of IETE Student Forum, SKN Sinhgad College of Engineering, Korti, Pandharpur on 13th Jul., 2018. The function was presided over by Prof. A. O. Mulani.



The purpose of ISF is to give a platform for the student members to exchange ideas by organizing/participating technical programs and activities. They would get the benefit for technical visits / practical training / project work in R&D laboratories/ industries that increases their spirit of self-reliance and professionalism.

The goal of the webinar was to educate young engineers on the latest skill set requirement of

the semiconductor industry and provides them the knowledge of massive growth of industry which is leading to huge demand of qualified and skilled verification and design engineers. The webinar was fully interactive. They explained in detail RTL design in VLSI. They demonstrated the RTL design and Functional Verification to the students. They opened the most emerging area of VLSI to the students. Students responded to the program to a great extent.

Ms. Badave Aishwarya, student member of ISF expressed her views about the program.



The function was concluded with vote of thanks by Mr. Sachin Kolekar and lot of promises and hope for future.